

Refine Search

Search Results -

Terms	Documents
L1 same (cache near5 processor near5 memory near5 (peripheral or (I adj1 O) or (input adj1 output)))	15

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L4

Refine Search

Recall Text

Clear

Interrupt

Search History

 DATE: Tuesday, August 10, 2004 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
<i>DB=USPT,USOC; PLUR=YES; OP=OR</i>			
<u>L4</u>	L1 same (cache near5 processor near5 memory near5 (peripheral or (I adj1 O) or (input adj1 output)))	15	<u>L4</u>
<u>L3</u>	L1 and (cache near5 processor near5 memory near5 (peripheral or (I adj1 O) or (input adj1 output)))	93	<u>L3</u>
<u>L2</u>	L1 and (cache near5 processor)	878	<u>L2</u>
<u>L1</u>	((shared or common) adj1 memory) same (multiprocessor or (multi adj1 processor))	1810	<u>L1</u>

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L4	0

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Search:

L5

Search History

DATE: Tuesday, August 10, 2004 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u> <u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<u>L5</u> L4	0	<u>L5</u>
<i>DB=USPT,USOC; PLUR=YES; OP=OR</i>		
<u>L4</u> L1 same (cache near5 processor near5 memory near5 (peripheral or (I adj1 O) or (input adj1 output)))	15	<u>L4</u>
<u>L3</u> L1 and (cache near5 processor near5 memory near5 (peripheral or (I adj1 O) or (input adj1 output)))	93	<u>L3</u>
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END OF SEARCH HISTORY